

FIG. I

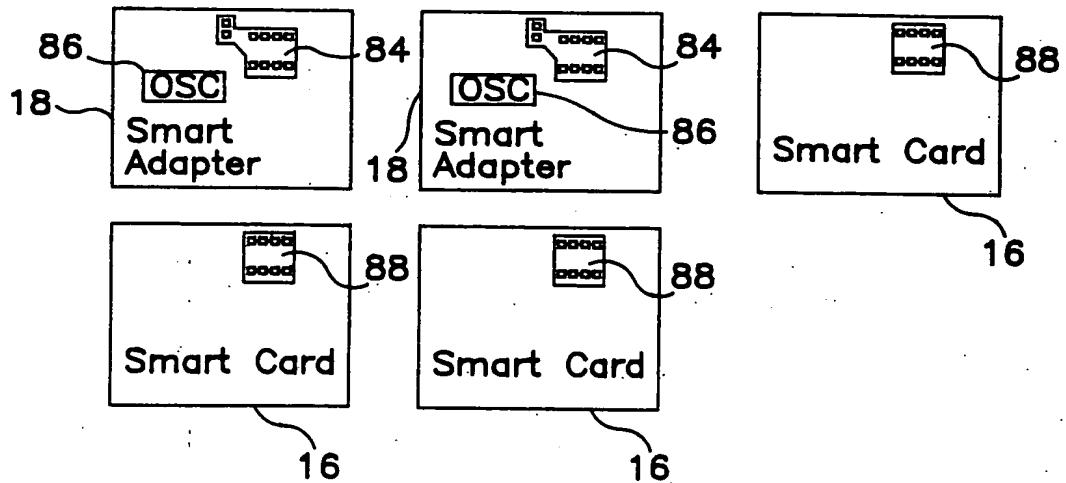
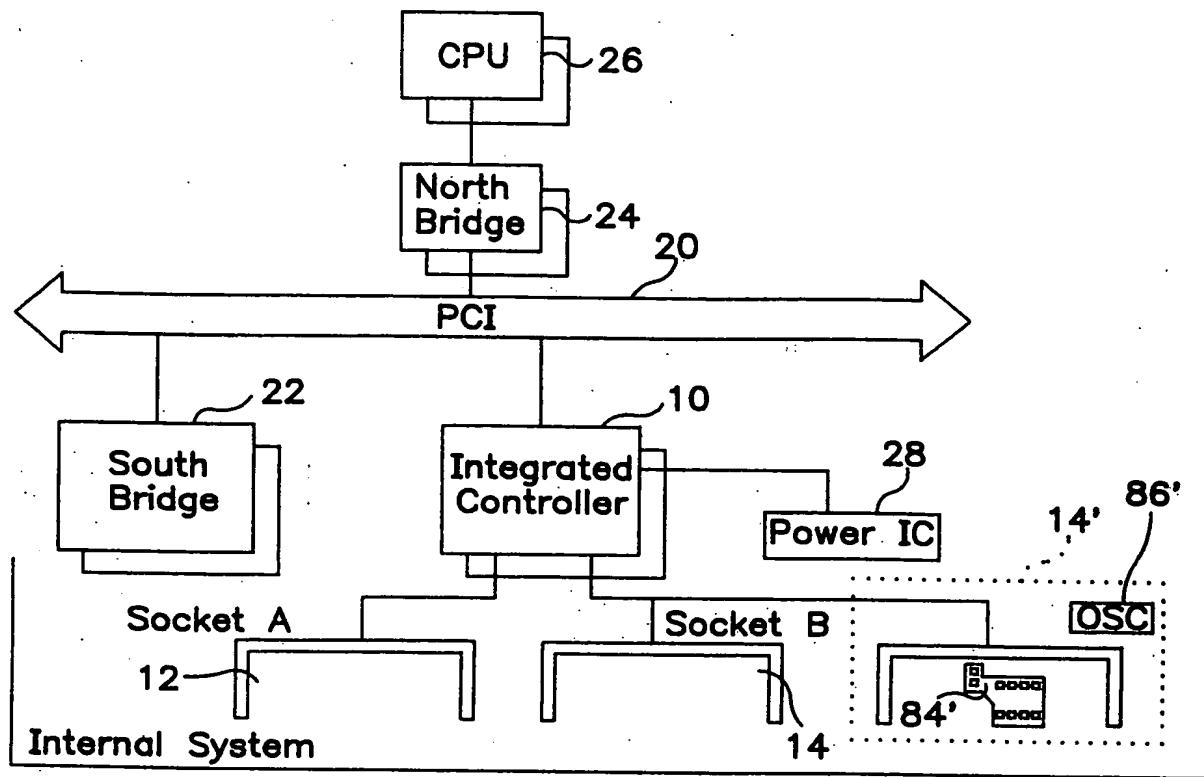
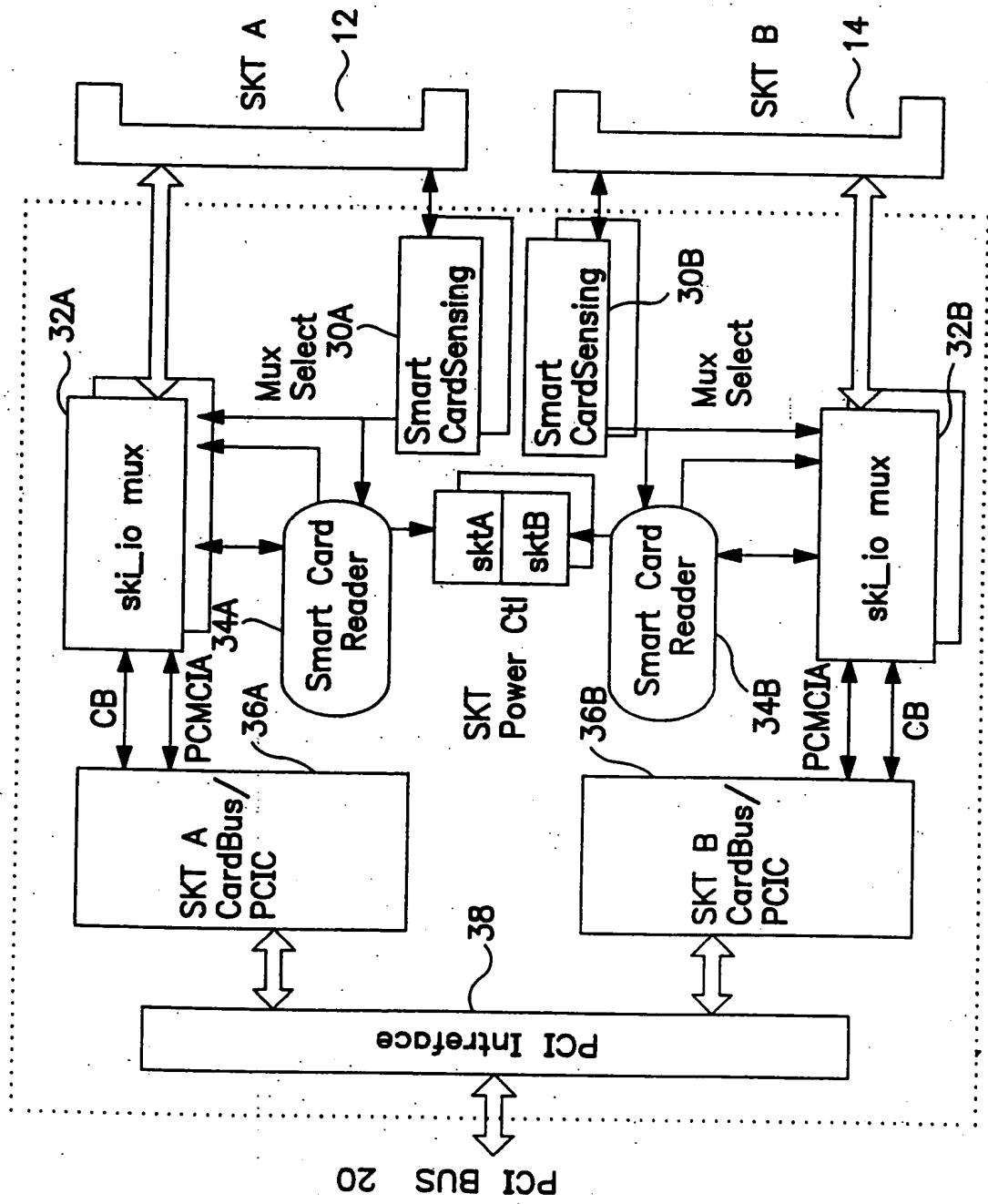


FIG. 2



3
FIG.

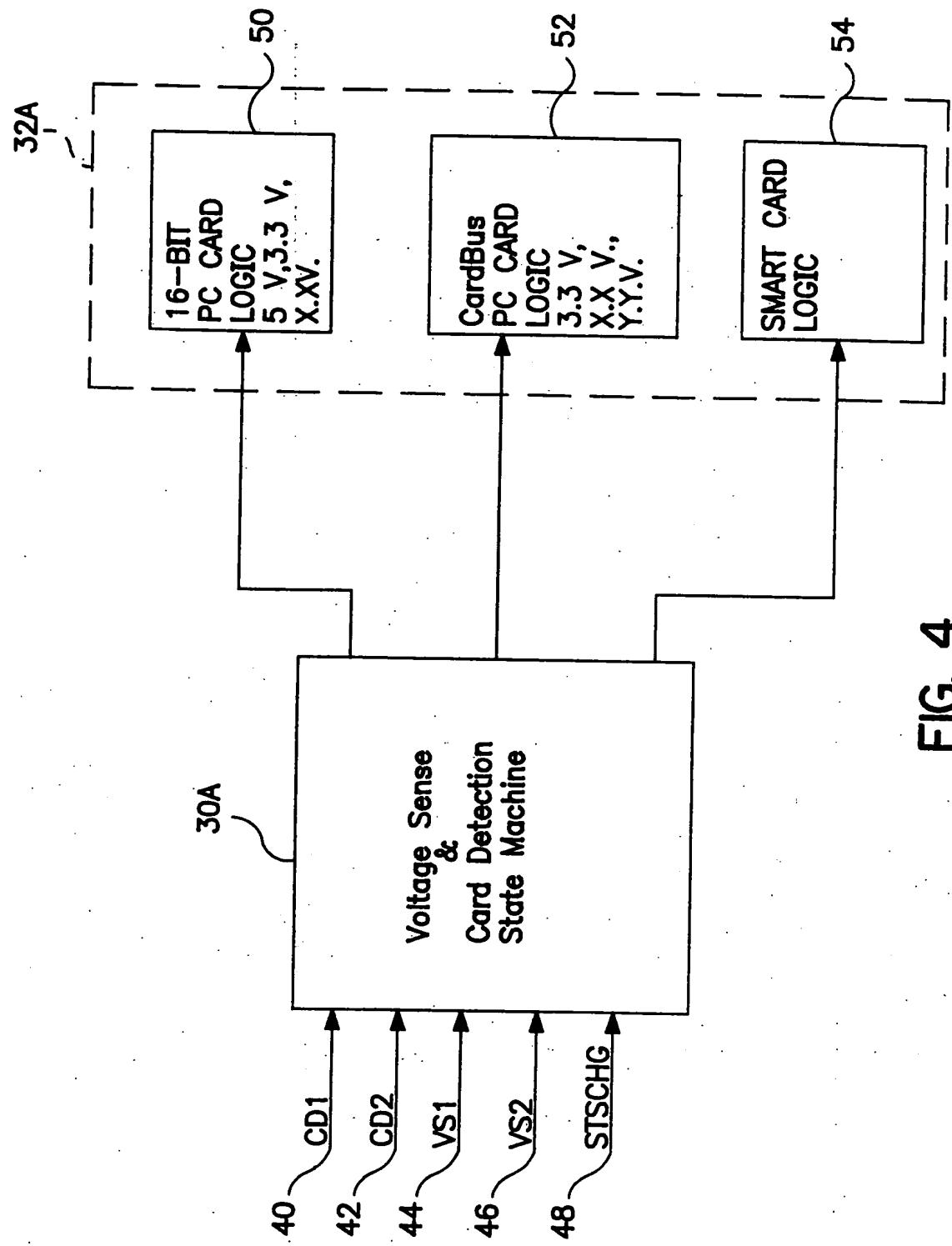


FIG. 4

CD2#/CCD2# (pin 67)	CD1#/CCD1# (pin 36)	VS2#/CVS2 (pin 57)	VS1#/CVS1 (pin 43)	Card Type
ground	ground	open	open	Key Interface Voltage
ground	ground	open	ground	5V 16-bit PC Card 5 V
ground	ground	ground	ground	5V 16-bit PC Card 5 V and 3.3 V
ground	ground	open	ground	5V 16-bit PC Card 5 V and 3.3 V and X.XV
ground	connect to CVS1	open	connect to CCD1#	LV 16-bit PC Card 3.3 V
ground	connect to CVS2	ground	ground	LV 16-bit PC Card 3.3 V and X.XV
connect to CVS1	ground	connect to CCD2#	ground	LV CardBus PC Card 3.3 V and X.XV
connect to CVS2	ground	ground	connect to CCD2#	LV CardBus PC Card 3.3 V and X.XV
ground	ground	ground	connect to CCD1#	LV CardBus PC Card 3.3 V and X.XV
connect to CVS2	ground	connect to CCD2#	open	LV 16-bit PC Card X.X V
ground	connect to CVS1	open	connect to CCD2#	LV CardBus PC Card X.X V
ground	connect to CVS1	ground	connect to CCD1#	LV CardBus PC Card X.X V and Y.Y V
ground	connect to CVS2	connect to CCD2#	open	LV CardBus PC Card X.X V
ground	connect to CVS1	open	connect to CCD2#	Y.Y V
ground	connect to CVS2	connect to CCD1#	ground	reserved
ground	connect to CVS1	ground	connect to CCD1#	reserved

FIG. 5

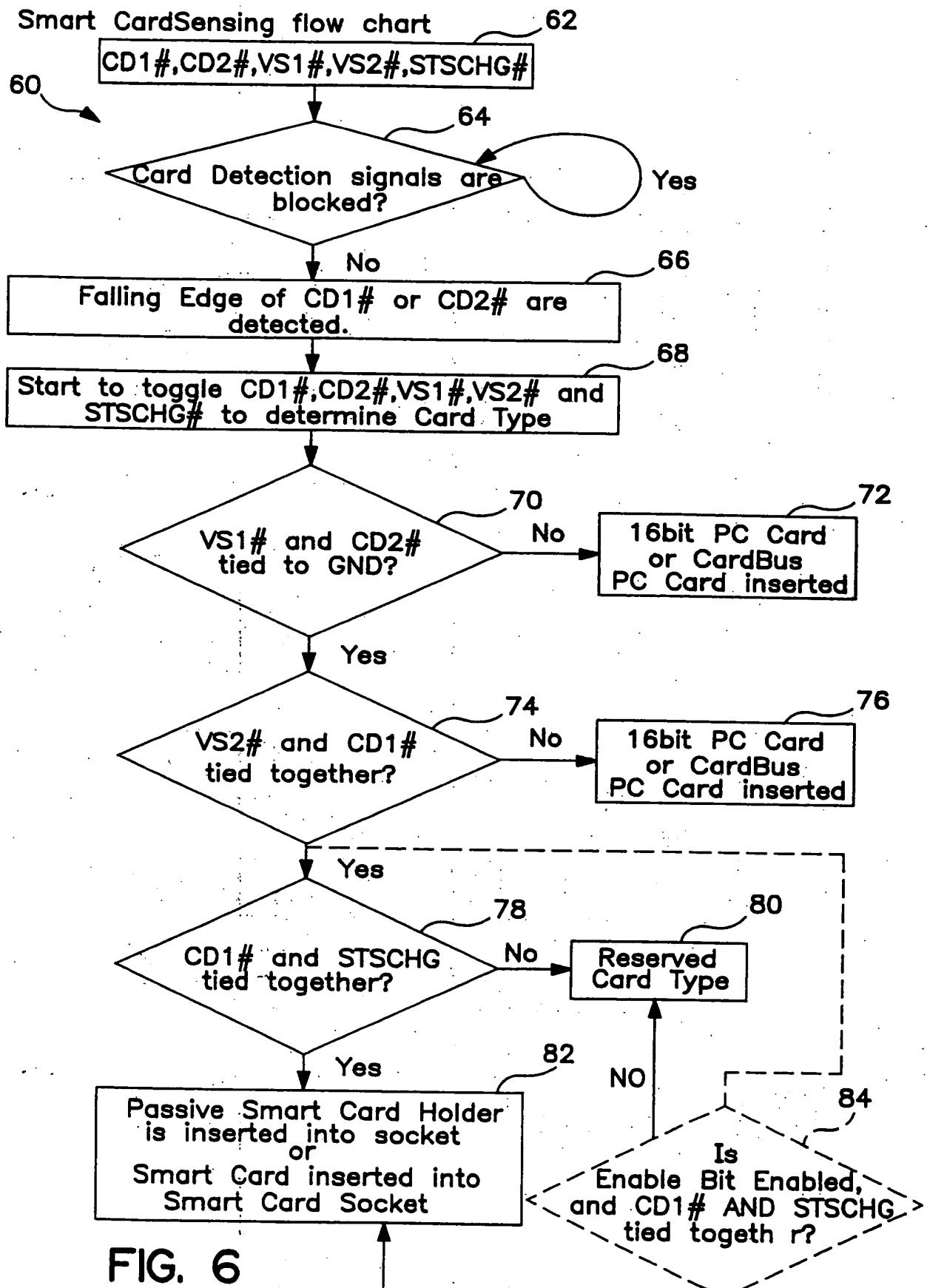


FIG. 6

Smart Card interface signals

PIN PCMCIA	Legacy Devices				Smart Card	
	16-bit PC Card		CardBus PC Card		Smart Card	
	Signal	I/O	Signal	I/O	Signal	I/O
17, 51	VCC		VCC		VCC	
58	RESET	O	CRST#	O	RESET	O
47	A18	O	Reserved		CLOCK	O
32	D2	I/O	Reserved		Rvd_C4	O
	GND		GND		GND	
18	VPP1		VPP1		VPP	
16	IREQ#	I	CINT#	I	I/O	I/O
40	D14	I/O	Reserved		Rvd_C8	O

FIG. 7A

Smart Card Detection signals

PIN PCMCIA						
	16-bit PC Card		CardBus PC Card		Smart Card	
	Signal	I/O	Signal	I/O	Signal	I/O
63	STSCHG#	I	CSTSCHG	I	Smart Card	I
36	CD1#	I	CCD1#	I	CD1#	I
43	VS1#	I	CVS1#	I/O	VS1#	I/O
57	VS2#	I	CVS2#	I/O	VS2#	I/O
67	CD2#	I	CCD2#	I	CD2#	I

FIG. 7B